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APPLICATION NUMBER	FILING or 371(c) DATE	GRP ART UNIT	FIL FEE REC'D	ATTY.DOCKET.NO	TOT CLAIMS	IND CLAIMS
10/572,185	03/16/2006	2816	1460	NAA236	13	2

## CONFIRMATION NO. 2595

25271  
GALLAGHER & LATHROP, A PROFESSIONAL CORPORATION  
601 CALIFORNIA ST  
SUITE 1111  
SAN FRANCISCO, CA94108

## FILING RECEIPT

Date Mailed: 03/07/2007

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. **If an error is noted on this Filing Receipt, please mail to the Commissioner for Patents P.O. Box 1450 Alexandria Va 22313-1450. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).**

### Applicant(s)

Toshiaki Sawada, Tokyo, JAPAN;

### Assignment For Published Patent Application

Advantest Corporation, Tokyo, JAPAN

**Power of Attorney:** The patent practitioners associated with Customer Number 25271

### Domestic Priority data as claimed by applicant

This application is a 371 of PCT/JP04/13460 09/15/2004

### Foreign Applications

JAPAN 2003-326549 09/18/2003

### If Required, Foreign Filing License Granted: 02/16/2007

The country code and number of your priority application, to be used for filing abroad under the Paris Convention, is **US10/572,185**

**Projected Publication Date:** 05/31/2007

**Non-Publication Request:** No

**Early Publication Request:** No

### Title

Semiconductor switch circuit

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